

Subject Code: H3802/R13

M. Tech –II Semester Regular/ Supply Examinations, October, 2015

ADVANCED COMPUTER ARCHITECTURE

(Common to DECS, E&CE and DECE)

Time: 3 Hours

Max Marks: 60

Answer any FIVE questions

All questions carry EQUAL marks

1. (a) Write about Amdahl's Law with relevant example.
(b) Explain about Quantitative principles of computer design.
2. (a) Describe about Alpha 21264 data cache with a neat sketch.
(b) Write about reducing cache miss penalty
3. (a) Describe briefly about Dynamic Scheduling.
(b) Write about Branch target buffers.
4. (a) Write about the taxonomy of parallel architectures.
(b) Describe about Synchronization.
5. (a) Explain about generic interconnection network.
(b) Describe briefly about Clusters with examples.
6. (a) Explain about Virtual memory.
(b) Write the difference between SRAM and DRAM Technologies.
7. (a) Describe about Static branch prediction.
(b) Write about the Compiler level techniques.
8. (a) Describe about the operands for media and signal process.
(b) Explain about Trimedia TM 32 CPU with a neat sketch.
